

ABSTRACT OF THE DISCLOSURE

In a writing device for writing data at 1-time or higher recording rates, a first delay circuit capable of variably controlling a delay time in units of a relatively long time and a second delay circuit capable of variably controlling a delay time in units of a relatively short time are employed in a combined manner. A laser drive pulse can be controlled in the direction of the time base with high accuracy corresponding to any recording rates ranging from 1-time to higher, e.g., 8- or 12-time, speeds without using very large number stages of delay gates.

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